



Kerala Public Enterprises (Selection and Recruitment) Board

Opposite KELTRON, Vellayambalam, Thiruvananthapuram

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Version: D

Name of Examination: Electrical Engineering

Date: 13/09/2026

Written examination for:

051-2025-Senior Engineer – Electrical (Executive Trainee – Electrical)-The Travancore Cochin Chemicals Limited

PROVISIONAL ANSWER KEY

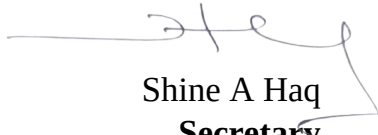
Sl No		Answer
1	A	0.30
2	B	may or may not be stable
3	C	0.707 lead
4	C	Hydro-electric power plants
5	C	SI – A = 0
6	B	entire z plane except $z = 0$ and $z = \infty$
7	D	Restriking Voltage
8	A	1
9	A	prevent hunting and to provide starting torque
10	B	Opposite to motoring
11	A	over compensation due to friction
12	B	Periodic with a period T
13	C	It doubles
14	B	Slip
15	B	Junction and stray capacitances
16	C	Rotor reactance=rotor resistance
17	A	0.5
18	C	PWM rectifier
19	A	0.5
20	C	Voltage controlled device

21	D	2
22	A	Output resistance
23	A	Induction motor drives
24	D	BJT in common base configuration has high voltage gain
25	C	6.66 A
26	C	It introduces a constant steady-state error (offset)
27	A	the existence of stray capacitance between the metallic junctions of the units and the tower body
28	D	$f < f_0$
29	D	Dual slope
30	C	400 MW
31	C	0.1 s
32	B	8 V
33	D	960
34	B	Non-linear
35	C	3 ms
36	A	$50 e^{-3t}$
37	C	Memory addressing
38	C	more than the no-load voltage
39	D	Load greater than generation
40	B	differentially compounded motor
41	D	become 3 times
42	C	3600 W
43	A	50 W
44	C	50%
45	B	Equal to 1
46	C	Interconnect two asynchronous AC grids
47	B	8 kHz
48	D	2.8 pu
49	A	Ratio of average load to maximum demand
50	A	$(1010)_2$

51	D	Damper winding
52	C	15, 16
53	D	Exponentially bounded
54	C	To reduce $I^2 R$ power losses in transmission lines
55	D	stability and voltage profile
56	D	0.4
57	D	XOR gate
58	C	both base-emitter and base-collector junction are forward biased
59	A	Zero
60	A	Prime mover input
61	D	Poles and signal type
62	C	Reduce steady state error
63	D	Solar Cell
64	D	2.5%
65	C	0°
66	A	Decreases
67	D	4 rad/s
68	C	all of these
69	B	demagnetising
70	B	1110 rpm
71	A	Voltage divider bias
72	A	6f
73	C	resistors of same values across each SCR
74	A	Overhead line insulators
75	D	zero
76	A	Differential gain to common-mode gain
77	D	125 W
78	D	Be positive and non-zero
79	D	6.3 V
80	B	Shift right by 3, then scale (compress) by 2
81	C	Core loss, windage and friction loss

82	D	39.6 Ω
83	B	within the unit circle
84	C	The same whether referred to the primary or secondary side
85	C	0.6A
86	D	0.08
87	D	NOR with negative logic
88	C	decrease both rise time and over shoot
89	A	67.C8
90	B	gives a nearly sinusoidal output voltage
91	A	Phase difference
92	C	2820 rpm
93	B	Reduced harmonic distortion
94	B	1.47
95	A	5 A, 3 A
96	B	1800mH
97	D	active power to busbars and its power angle will increase
98	A	Deactivating independent sources and finding resistance
99	D	inductive reactance
100	A	400 MW

NB: - Complaints regarding the Provisional Answer Key should be submitted by candidates to the e-mail (kpesrb@gmail.com) of the Board within a period of 4 days from the date of publication of this Provisional Answer Key (both days included) ie. By 11.59 PM on 17/09/2026. Final Answer Key will be published in the official website of KPESRB.


 Shine A Haq
 Secretary